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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Raj Kumar Jain

Serial No. 10/065,220

Filed:

For:

September 26, 2002

INTEGRATED CIRCUITS WITH PARALLEL SELF-

TESTING

Examiner:

Thong Quoc LE

Art Unit:

2818

Atty Dkt No.:

00P19153US

Dec 05, 2002

CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, Washington, DC 20231 on

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Signed:

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COMMISSIONER FOR PATENTS Washington, D.C. 20231

## INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following reference to the attention of the Examiner. The reference is listed on the attached modified PTO Form No. PTO/SB/08A. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

A copy of each patent, publication or other information listed on the modified PTO Form No. PTO/SB/08A is enclosed, except as otherwise indicated.

Dated: Dec 05, 2002

Respectfully submitted,

Dexter Chin

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